

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Mitsutoshi MIYASAKA

Application No.: New U.S. Patent Application

Filed: February 26, 2004

Docket No.: 118490

For: COMPLEMENTARY THIN FILM TRANSISTOR CIRCUIT, ELECTRO-OPTICAL DEVICE, AND ELECTRONIC APPARATUS

INFORMATION DISCLOSURE STATEMENT

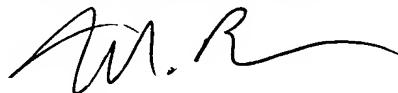
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- 2. Relevance of references 3-4 is discussed in the present specification.
- 3. English-language Abstracts of the non-English language references are attached hereto. (Refs. 1-2)
- 4. Computer-generated English translations of the following Japanese Patent Publication have been obtained from the website of the Japanese Patent Office ([<http://www.jpo.go.jp>]), and are attached, but have not been reviewed for accuracy. See References 1-2.

Respectfully submitted,



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Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE	ATTY DOCKET NO. 118490	APPLICATION NO. New U.S. Patent Application		
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		APPLICANT Mitsutoshi MIYASAKA				
		FILING DATE February 26, 2004				
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
FOREIGN PATENT DOCUMENTS						
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
	1	JP A 2003-289040 w/ abst & trans	10/10/2003	Japan		
	2	JP A 9-172183 w/ abst & trans	06/30/1997	Japan		
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)						
	3	"TBD: Single Crystal Thin Film Transistors" Webpage. <i>IBM Technical Disclosure Bulletin</i> August 1993, pp. 257-258 http://www.delphion.com/t dbs/tbd?&order=93A+61960				
	4	Ishihara, Ryoichi et al. "Advanced Excimer-Laser Crystallization Techniques of Si Thin-Film for Location Control of Large Grain on Glass." <i>Flat Panel Display Technology and Display Metrology II</i> Proceedings of SPIE Vol. 4295. 2001, pp. 14-23				
EXAMINER					DATE CONSIDERED	
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.						